# Cadence Palladium User Manual

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*Cadence Palladium Z1 Enterprise*

*Emulation Platform*

Page 1 CADE N C E PA LLA D IU M XP
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The Cadence ® Accelerated Verification IP (AVIP) enables design acceleration using the Cadence Palladium ® Verification Computing Platform, and supports a variety of use modes to enable both validation of systems and subsystems, as well as more extensive protocol compliance verification of block- and IP-level designs.

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